

What Is Claimed Is:

1. A motor vehicle control unit, in particular an engine control unit (11), comprising a processor (12) and a first interface (14) for the communication with functional units (16) of the motor vehicle, wherein at least one second interface (17) is combined with the processor (12) in one sub-assembly.
2. The motor vehicle control unit as recited in Claim 1, wherein the interface (17) is able to access a storage module (13) of the motor vehicle control unit without participation of the processor (12).
3. The motor vehicle control unit as recited in Claim 1 or 2, wherein the interface (17) is able to access the code of the processor (12) in a storage module (13) for writing purposes.
4. The motor vehicle control unit as recited in one of the preceding claims, wherein the second interface (17) is configured for the block transfer of data.
5. The motor vehicle control unit as recited in one of Claims 1 through 4, wherein the first interface (17) is combined with the processor (12) and the second interface (14) in the same sub-assembly.
6. The motor vehicle control unit as recited in one of the preceding claims, wherein the sub-assembly is a printed-circuit board.
7. The motor vehicle control unit as recited in one of Claims 1 through 5, wherein the sub-assembly is a semiconductor chip.
8. The motor vehicle control unit as recited in one of the preceding claims, wherein it includes at least one storage (13) for operating parameters of the processor, and the storage (13) is able to be written on and/or read out via the second interface (17).
9. The motor vehicle control unit as recited in one of the preceding claims, wherein the second interface (17) is a serial interface.

10. The motor vehicle control unit as recited in Claim 8, wherein the second interface is an ethernet or FireWire interface.
11. The motor vehicle control unit as recited in Claim 9, wherein the second interface (17) is a USB interface.
12. The motor vehicle control unit as recited in Claim 11, wherein the second interface (17) is able to transmit data received from the processor (12) via the first interface (14), in isochronous mode.
13. The motor vehicle control unit as recited in Claim 11, wherein the second interface (17) is able to transmit control parameters of the processor (12) in bulk mode.
14. The motor vehicle control unit as recited in Claims 8 and 11, wherein the interface (17) is able to read and/or write to individual storage locations of the storage (13) in the interrupt mode.
15. A motor vehicle having a motor vehicle control unit (11) as recited in one of the preceding claims, wherein the second interface (17) is connected to none of the functional units of the motor vehicles that are to be controlled.
16. A method for communication between a motor vehicle control unit as recited in one of Claims 11 through 14 and an external host (18) in which the host (18) stipulates different USB endpoints and transmission modes for different types of data to be exchanged between it and the motor vehicle control unit.
17. The method as recited in Claim 16 in which the host polls the endpoints according to a priority sequence.

## Summary

A motor vehicle control unit, in particular an engine control unit (11), includes a processor (12), a first interface (14) for the communication with functional units (16) of the motor vehicle and at least one second interface (17), which is combined with the processor (12) in a

5 sub-assembly.

(Figure 2)